Augmenting Natural Language Generation with External Memory

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Introduction

- Previous approaches:
  - Rule-based and Corpus-based approaches
  - State-of-art RNN based generator:
    - Jointly optimize sentence planning and surface realization
    - Fewer heuristics but more natural, efficient and diverse system output
- Motivation for memory module:
  - Better capture long-term dependencies
  - Zero-shot or k-shot learning
  - Improve variability of the output

Semantically Conditioned LSTM-based generator

The semantically conditioned LSTM generator[1] is based on a recurrent NN architecture in which a 1-hot encoding of a token \( w_t \) is input at each time step conditioned on a recurrent hidden layer \( h_t \) and outputs the probability distribution of the next token \( w_{t+1} \). This way, the network can produce a sequence of tokens which can be lexicalized to form the required utterance.

The LSTM architecture can be defined as below:

1. \[ h_t = \sigma(W_{wh} w_t + W_{hh} h_{t-1}) \]
2. \[ f_t = \sigma(W_{wf} w_t + W_{hf} h_{t-1}) \]
3. \[ \tilde{c}_t = \tanh(W_{wc} w_t + W_{hc} h_{t-1}) \]
4. \[ c_t = f_t \odot c_{t-1} + i_t \odot \tilde{c}_t \]
5. \[ h_t = o_t \odot \text{tanh}(c_t) \]

The generator introduces a sentence planning cell based on a sigmoid control gate and a dialogue act(DA):

1. \[ r_t = \sigma(W_{wr} w_t + \alpha W_{rh} h_{t-1}) \]
2. \[ d_t = r_t \odot d_{t-1} \]
3. \[ c_t = f_t \odot c_{t-1} + i_t \odot \tilde{c}_t + \text{tanh}(W_{dc} d_t) \]

Using external memory to augment generation models

Using external memory module has been studied extensively recently. Memory in the neural network has the capability to better monitor long-term dependencies and to store useful information from previous training examples. The interaction of the memory module with the environment involves read and write operations which are based on a associated attention mechanism. A few things can be considered when designing the memory module:

- Ways to address the memory:
  - Content-based addressing: access memories based on the similarity to a given cue
  - Location-based addressing: access memories based on their position
  - Combination of both

- Initialization of the memory:
  - Learned memory: treat memory as model parameters, initialized randomly
  - Fixed memory: memory is seen as the database of additional information

Soft/Hard attention mechanisms:

- Make the memory module fully-differentiable or not
- Which layer to use memory and how to use the output from the memory module

Some comparisons and results

The metrics used to evaluate system performance in our task are Slot Error Rates, BLEU-4 scores and perplexity. Averaged results over 5 random seeds for Restaurant Domain are summarized as below:

<table>
<thead>
<tr>
<th>Model Type</th>
<th>Slot Error(%)</th>
<th>BLEU-4</th>
</tr>
</thead>
<tbody>
<tr>
<td>Vanilla LSTM</td>
<td>12.21</td>
<td>0.525</td>
</tr>
<tr>
<td>SC-LSTM</td>
<td>5.49</td>
<td>0.537</td>
</tr>
<tr>
<td>CVAE+SC-LSTM</td>
<td>4.86</td>
<td>0.539</td>
</tr>
<tr>
<td>Memory module for CVAE</td>
<td>4.38</td>
<td>0.546</td>
</tr>
</tbody>
</table>

Table: Averaged Results for different generation models

The bottom model architecture uses an external memory module to augment the Conditional VAE. The memory is initialized randomly and uses soft content-based attention mechanisms.

Future Experiments

- Different memory mechanisms
  - Key-value style memory module
  - memory module as trainable parameters (a combination of memory keys and values)

- Using dynamic external memory module augmenting LSTM decoder

References